Instructor: Oluwayomi Adamo - B 208
Phone: 940-891-6874, oluwayomi.adamo@unt.edu
Class Hours: Tues. 4:00 PM – 6:50 PM
Office Hours: Tues. 10:00 AM - 12:01 PM
Course Venue: NTDP B 217
Course Website: www.ee.unt.edu/public/adamo/EENG2710_S10
Teaching Assistant: Avinash Kakarala
Office Hours: Tues. 3:00 PM – 4:00 PM and Weds. 2:00 PM – 3:00 PM
Prerequisite: MATH 1710

Course (Catalog) Description
Digital computers and digital information processing systems; Boolean algebra, principles and methodology of logic design; machine language programming; register transfer logic; microprocessor hardware, software and interfacing; fundamentals of circuits and systems; computer organization and control; memory systems, arithmetic unit design.

Textbook(s) and/or other required material

Course Objective & Learning Outcome
The main objective of the course is to facilitate the student in gaining a solid foundation in Communications System so that after completing the course students will able to:
- Demonstrate knowledge of Digital and Analog Systems,
- Demonstrate an understanding of Number Systems and Digital Logic Gates,
- Demonstrate knowledge of Boolean Algebra, Switching Functions and Canonical Forms,
- Carryout Combinational Circuit Minimization, Analysis, and Synthesis,
- Demonstrate knowledge of Sequential circuits elements and sequential logic circuits,
- Demonstrate knowledge of Modular Sequential Logic- Counters and shift registers,
- Carryout Minimal Design of Synchronous Sequential Circuits,
- Analyze and Design asynchronous sequential circuits,
- Carryout Digital Logic Testing.

General Policy
- Class attendance is mandatory. You will need to sign attendance sheet every class
- It is strongly encouraged to get to know each other in the class. Discussions on course materials are allowed!
- Everyone must turn in her/his own individual work. Simply copying other’s homework will be treated as a violation of academic honesty
- It is the responsibility of students with certified disabilities to provide the instructor with appropriate documentation from the Dean of Students Office (see http://www.unt.edu/oda)
- Please visit http://www.unt.edu/csrr/ for your rights and responsibilities

Grading Policy
- 3 Tests – 60%
- Assignments – 20%
- Labs – 5%
• Quizzes – 5%
• Project – 10%

Topics (Tentative)
1. Digital and analog systems- an introduction, historical perspective
2. Number systems and codes
3. Boolean Algebra, Switching functions and canonical forms
4. Circuit minimization, Analysis of combinational circuits, and Timing issues
5. Top-down Modular Design of Combinational Logic
6. Sequential Circuit Elements- Latches and flip-flops
7. Modular Sequential Logic- Counters and shift registers
8. Analysis and Design of synchronous sequential circuits
9. Analysis and Design of asynchronous sequential circuits
10. Digital Logic Testing